



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Daisaburo TAKASHIMA

SERIAL NO: 10/615,181

GAU:

FILED: July 9, 2003

EXAMINER:

FOR: SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.

A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.

A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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SHEET 1 OF 1

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 240128US2S	SHEET 1 OF 1		SERIAL NO. 10/615,181
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Daisaburo TAKASHIMA					
		FILING DATE July 9, 2003		GROUP			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
AA							
AB							
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
					YES	NO	
AO							
AP							
AQ							
AR							
AS							
AT							
AU							
AV							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	D. TAKASHIMA, et al., IEEE Journal of Solid-State Circuits, vol. 33, no. 5, pages 787-792, "HIGH-DENSITY CHAIN FERROELECTRIC RANDOM ACCESS MEMORY (CHAIN FRAM)", May 1998					
	AX	S. ONISHI, et al., IEDM, pages 843-846, "A HALF-MICRON FERROELECTRIC MEMORY CELL TECHNOLOGY WITH STACKED CAPACITOR STRUCTURE", 1994					
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							